



Atty. Docket No. 8020-1002-1  
PATENT

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Yoshiaki HISAMUNE et al. Conf. No. 3150

Serial No. 10/083,611 Group 2812

Filed February 27, 2002 Examiner Richard A. Booth

METHOD OF MANUFACTURING SEMICONDUCTOR  
DEVICE, NONVOLATILE SEMICONDUCTOR  
MEMORY DEVICE AND METHOD OF  
MANUFACTURING THE SAME

AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

January 8, 2004

Sir:

In response to the Official Action of October 16, 2003,  
please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing  
of claims which begins on page 2 of this paper.

**Remarks** begin on page 7 of this paper.